IN THE UNITED STATES PATENT & TRADEMARK OFFICE

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│ This paper and the papers referred to   │
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COMBINED STATEMENT UNDER 37 C.F.R. § 3.73(b)-ESTABLISHING RIGHT OF ASSIGNEE TO TAKE ACTION
and
POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST (REVOCATION OF PRIOR POWERS)

Commissioner for Patents
Washington, D.C. 20231

Sir:

Sun Microsystems, Inc., a corporation organized under the laws of Delaware states that it is the assignee of entire right, title, and interest; in the patents and patent applications listed on the attached APPENDIX A, by virtue of an Assignment from the inventor(s) of each of the patents and patent applications listed on the attached APPENDIX A. Date of Recordal, and Reel and Frame number of Recordal by the United States Patent & Trademark Office identified on the attached APPENDIX A.

Sun Microsystems, Inc., a Delaware Corporation, having principle place of business at 901 San Antonio Road, Palo Alto, California, 94303, as assignee of record of the entire interest of the above identified patent (per above statement under 37 CFR §3.73(b)), hereby states that all powers of attorney previously given are hereby revoked.

Further, the following attorneys and/or agents are hereby appointed to prosecute and transact all business in the Patent and Trademark Office connected therewith:

Peter B. Martine (Reg. No. 32,043)	Edmund H. Mizumoto (Reg. No. 46,938)
Albert S. Penilla (Reg. No. 39,487)	Joe A. Brock II (Reg. No. 46,021)
Rick von Wohld (Reg. No. 48,018)	Michael L. Gencarella (Reg. No. 44,703)
Chester E. Martine, Jr. (Reg. No. 19,711)	George B. Leavell, Esq. (Reg. No. 45,436)
Lygeri Kokkinakos (Reg. No. 42,756)	Fariba Yadigar-Bandari (Reg. No. P-53,805)
Kenneth D. Wright (Reg. No. P-53,795)	

provided that if any one of said attorneys ceases being affiliated with the law firm of MARTINE & PENILLA, LLP as partner or employee, such attorney's appointment as attorney and all powers derived therefrom shall terminate on the date such attorney ceases being so affiliated; and:

Marc D. Foodman (Reg. No. 34,110)	Monica D. Ward (Reg. No. 40,696)
Timothy J. Crean (Reg. No. 37,116)	Alexander E. Silverman (Reg. No. 37,940)
Naren Chaganti (Reg. No. 44,602)	Marilyn E. Glaubensklee (Reg. No. 35,521)
Anirma R. Gupta (Reg. No. 38,275)	Elaine Lee (Reg. No. 41,936)
Sean P. Lewis (Reg. No. 42,798)	Hugh H. Matsubayashi (Reg. No. 43,779)
Michael J. Schallop (Reg. No. 44,319)	Paul D. Sorkin (Reg. No. 39,039)
Bernice B. Chen (Reg. No. 42,403)	Andrew C. Chen (Reg. No. 43,544)
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The undersigned is authorized to act on behalf of the Assignee.

(Signature of authorized person)   12/13/02 (date)

Marilyn E. Glaubensklee
(type or print name of authorized person)
Assistant General Counsel, having delegated Power to sign on Behalf of Sun Microsystems, Inc.

Title of authorized person

Sun Microsystems, Inc.

3.73(b) POA Transfer

New Attys: Martine Penilla

SerialNumber	FilingDate	Patent	ReelAndFrame	CaseNo./Art Unit	ClientCaseNumber	Title	DateIssued OrAbandoned	Status
				2122-2187				
09/436,464	11/8/1999		010507/0041	SUNMP282/2122	P4373/MG	METHOD AND APPARATUS FOR INSERTING DATA PREFETCH OPERATIONS USING DATA FLOW ANALYSIS		Allowed
09/535,830	3/24/2000		010647/0907	SUNMP294/2122	P4678/MG	MODULO SCHEDULING VIA BINARY SEARCH FOR MINIMUM ACCEPTABLE INITIATION INTERVAL METHOD AND APPARATUS		Pending
09/679,945	10/4/2000		011207/0502	SUNMP301/2122	P5333/MG	USING VALUE-EXPRESSION GRAPHS FOR DATA-FLOW OPTIMIZATIONS		Pending
09/823,207	3/30/2001		011680/0581	SUNMP303/2122	P5446/MG	METHOD AND APPARATUS FOR SIMULTANEOUS OPTIMIZATION OF CODE TARGETING MULTIPLE MACHINES		Pending
10/027,876	12/21/2001			SUNMP264B/2123	P3434/MG	METHOD FOR SHIELDING LOGIC SIGNALS		Pending
09/390,071	9/3/1999		010244/0282	SUNMP269/2124	P3657/MG	QUOTIENT DIGIT SELECTION LOGIC FOR FLOATING POINT DIVISION/SQUARE ROOT		Pending
08/511,218	2/23/2000		010590/0484	SUNMP276/2124	P4006/MG	OPTIMIZED SYSTEM AND METHOD FOR PARALLEL LEADING ONE/ZERO ANTICIPATION		Pending
09/501,549	2/9/2000		010552/0568	SUNMP288/2124	P4508/MG	SYSTEM AND METHOD FOR FINE-GRAINED ANALYSIS OF COMPLEX SOFTWARE		Pending
09/586,657	6/1/2000		010842/0163	SUNMP295/2124	P4891/MG	PARALLEL GREATER THAN ANALYSIS METHOD AND APPARATUS		Pending
09/587,176	6/2/2000		010844/0259	SUNMP296/2124	P4892/MG	METHOD AND APPARATUS FOR COMPUTING PARALLEL LEADING ZERO COUNT WITH OFFSET		Pending
10/072,234	2/6/2002		012941/0774	SUNMP307/2131	P5554/MG	METHOD AND SYSTEM FOR PROVIDING A SECURED WIDE AREA NETWORK ACCESS		Pending
09/513,662	2/25/2000			SUNMP289/2133	P4555/MG	REINSTATE APPARATUS AND METHOD TO RECREATE DATA BACKGROUND FOR TESTING SRAM		Pending
09/590,786	6/8/2000		010857/0606	SUNMP292/2133	P4656/MG	SYSTEM AND METHOD FOR IMPLEMENTING MEMORY TESTING IN A SRAM UNIT		Pending
09/643,895	8/18/2000		011031/0288	SUNMP298/2171	P4914/MG	EXPLICIT HIERARCHICAL REGISTER FILE FOR PROCESSORS		Pending
09/239,903	1/29/2003	6,212,594	010131/0752	SUNMP260/2181	P3069/MG	...INTERRUPT PERIODS	4/3/2001	Issued
09/204,737	12/2/1998	6,253,332	9821/0013	SUNMP261/2182	P3090/MG	APPARATUS FOR GENERATING SHIFTED DOWN CLOCK SIGNALS	6/26/2001	Issued

APPENDIX A
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Sun Microsystems, Inc.

3.73(b) POA Transfer

New Attys: Martine Penilla

09/687,608	10/12/2000	6,334,180		SUNMP250C/2183	P2258CNT1/MG	PROCESSOR COUPLED BY VISIBLE REGISTER SET TO MODULAR COPROCESSOR INCLUDING INTEGRATED MULTIMEDIA UNIT	12/25/2001	Issued
09/642,075	8/18/2000		010014/0360	SUNMP299/2183	P4913/MG	EXPLICITLY CLUSTERED REGISTER FILE AND EXECUTION UNIT ARCHITECTURE		Pending
09/089,626	6/1/1998	6,263,399	9403/03348	SUNMP257/2187	P2858/MG	MICROPROCESSOR TO NAND FLASH INTERFACE	7/17/2001	Issued
09/564,202	5/3/2000		010789/0767	SUNMP287/2187	P4509/MG	ACTIVE ADDRESS CONTENT ADDRESSABLE MEMORY		Pending

APPENDIX A
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